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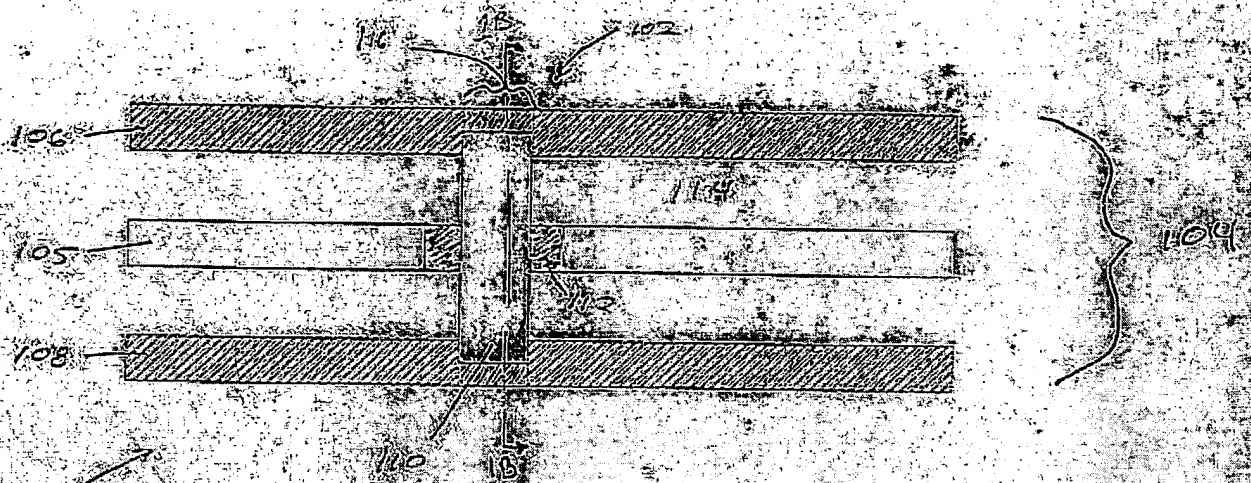


FIG. 1A

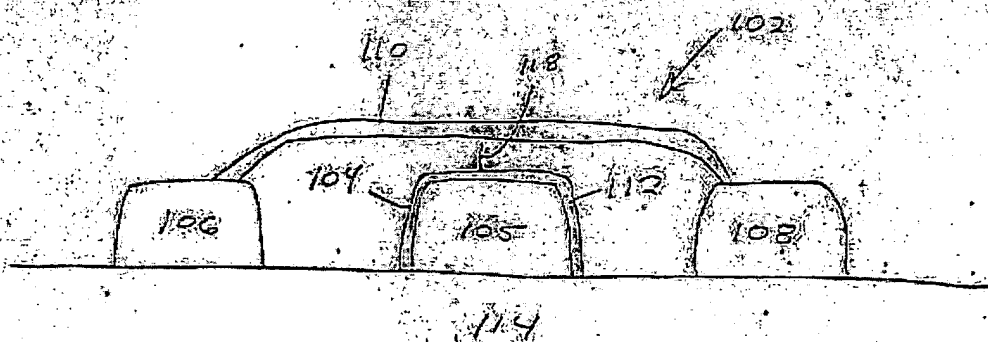
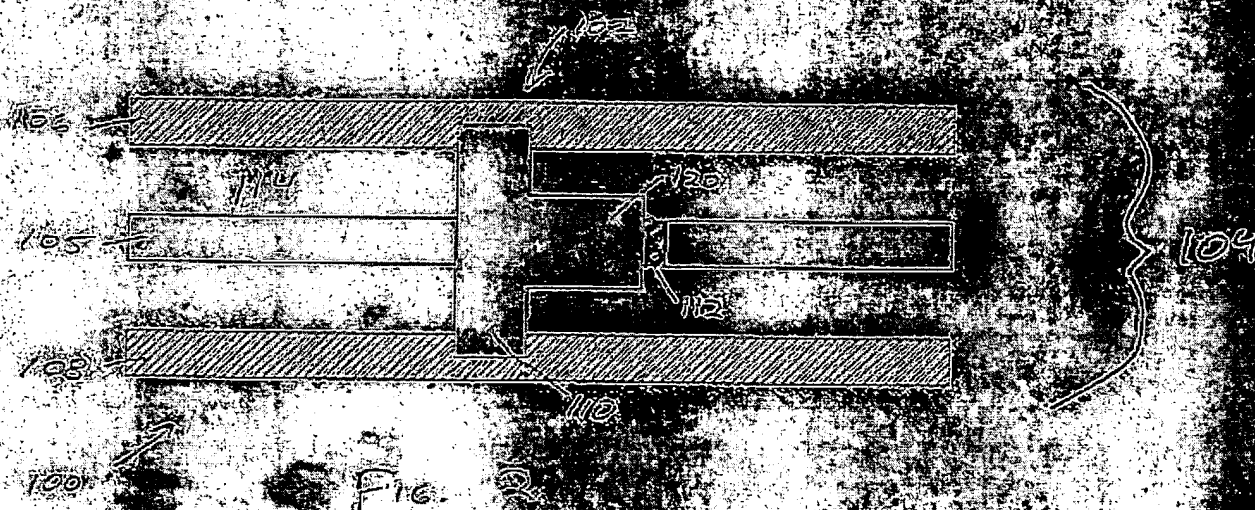


FIG. 1B



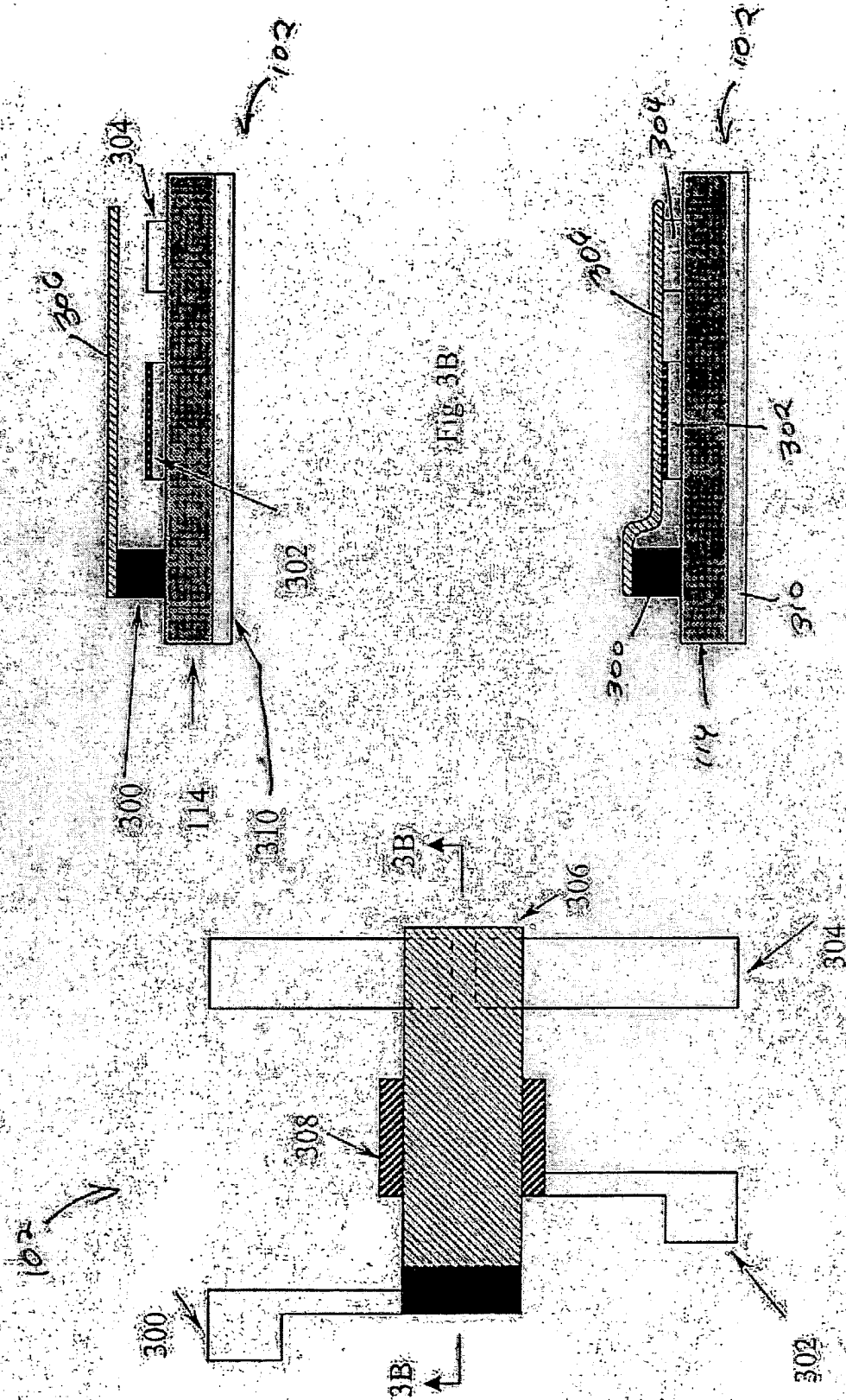
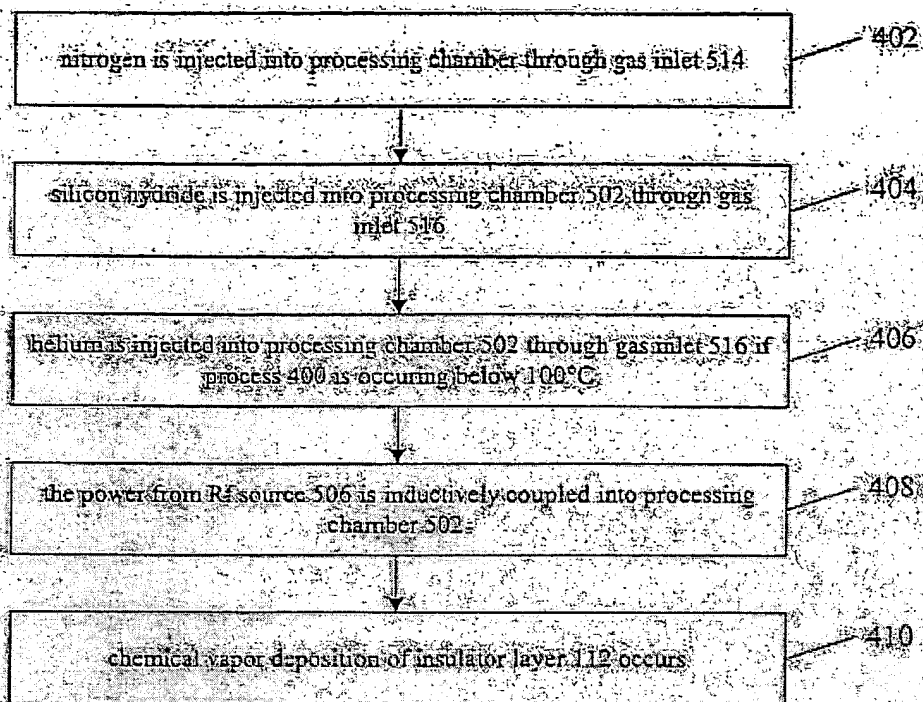


FIG 3B

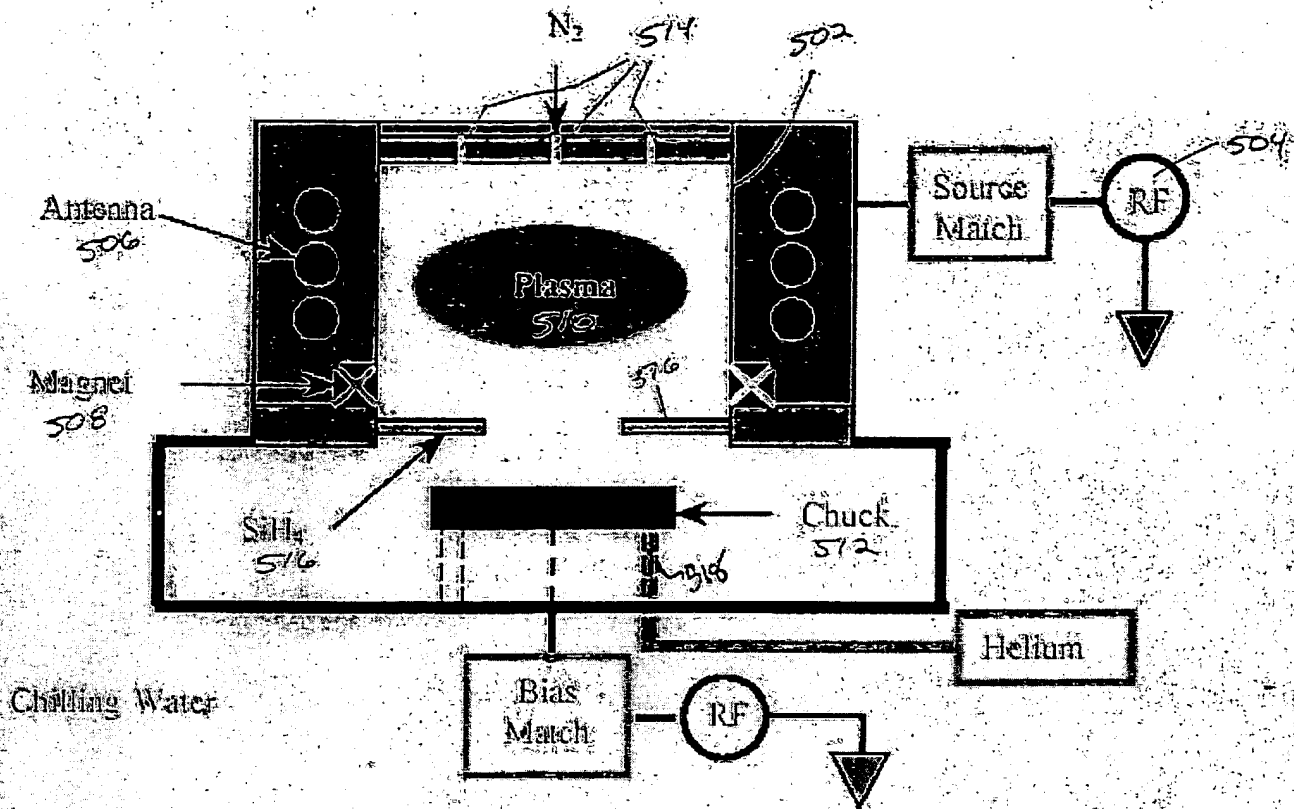
FIG 3A

FIG 3C



400 ↗

FIG. 4



500

FIG. 5

630

632

634

FIG. 6A



632

634

FIG. 6B



632

634

FIG. 6C

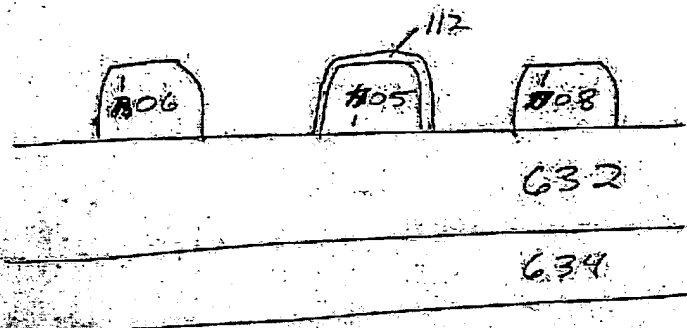


FIG. 6D

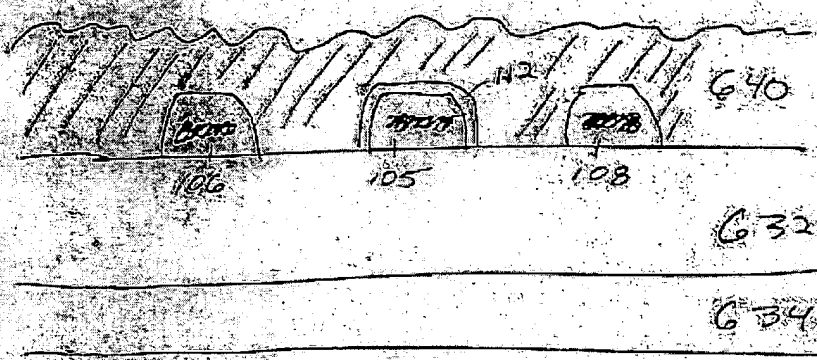


FIG. 6E

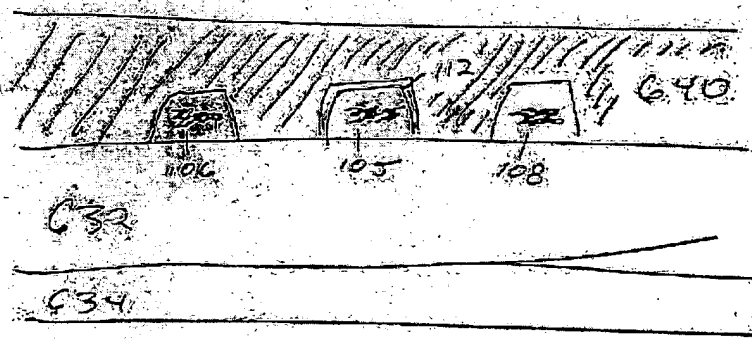


FIG. 6F

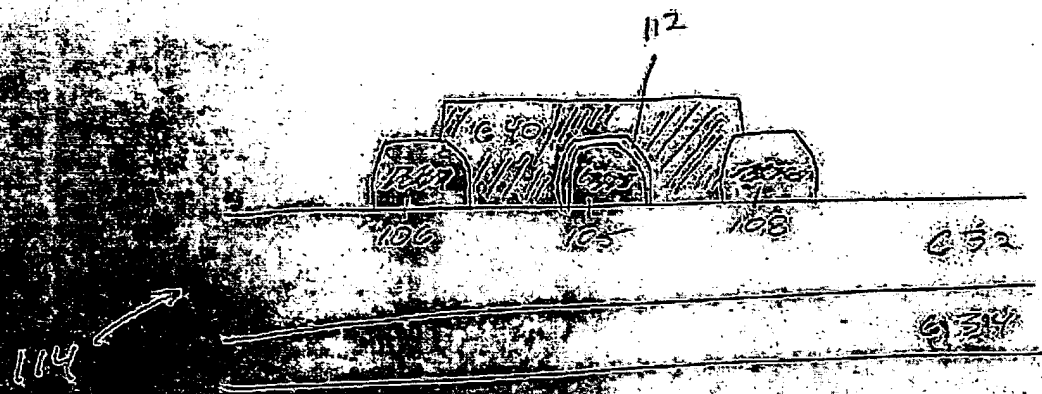


FIG. 6G



FIG. 6H

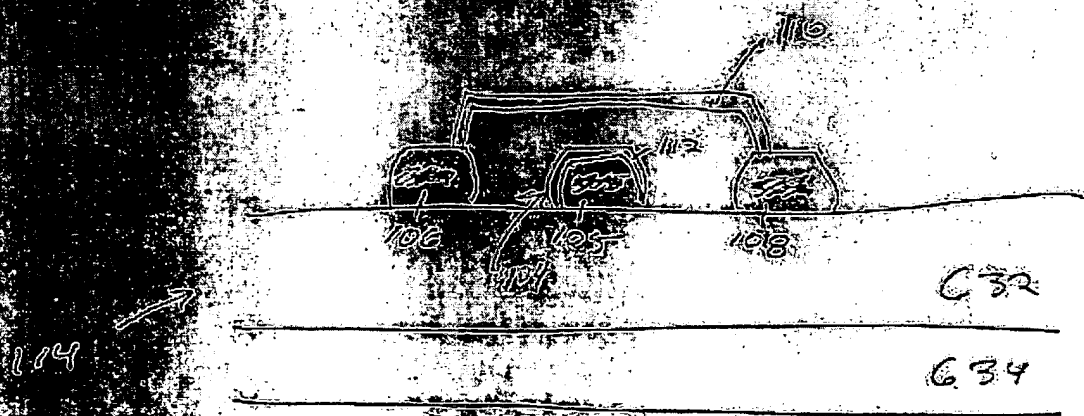


FIG. 6I

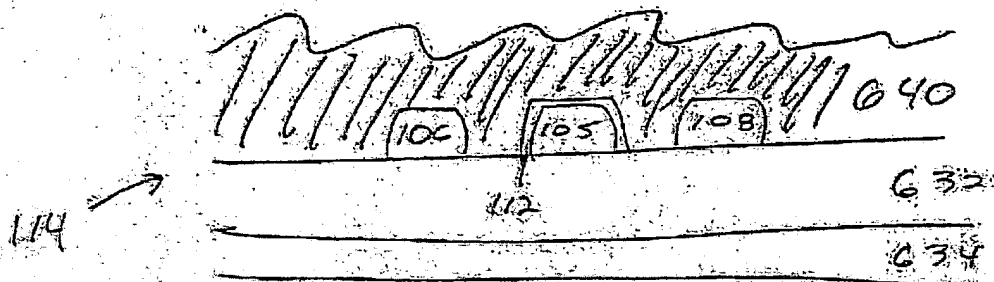


FIG. 7A

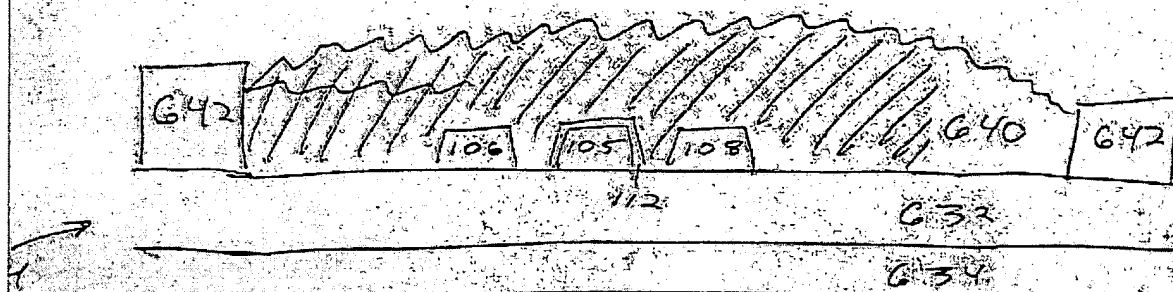


FIG. 7B

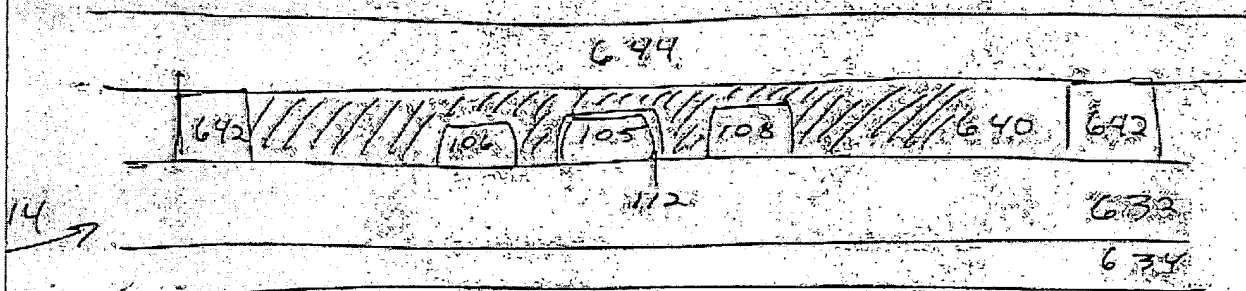


FIG. 7C

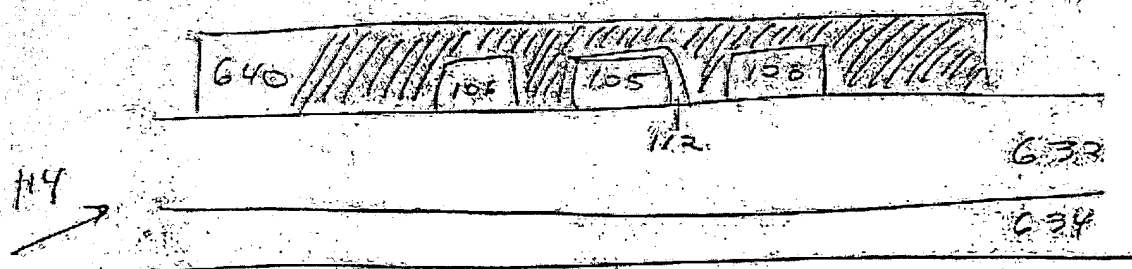
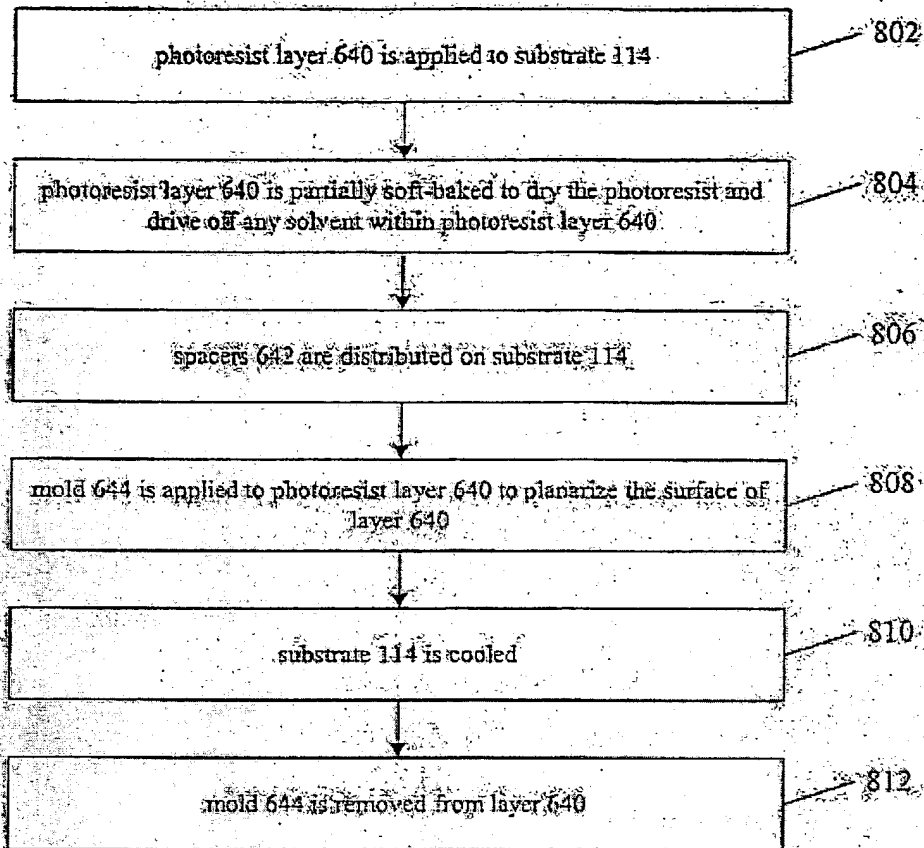


FIG. 7D



700

FIG. 8

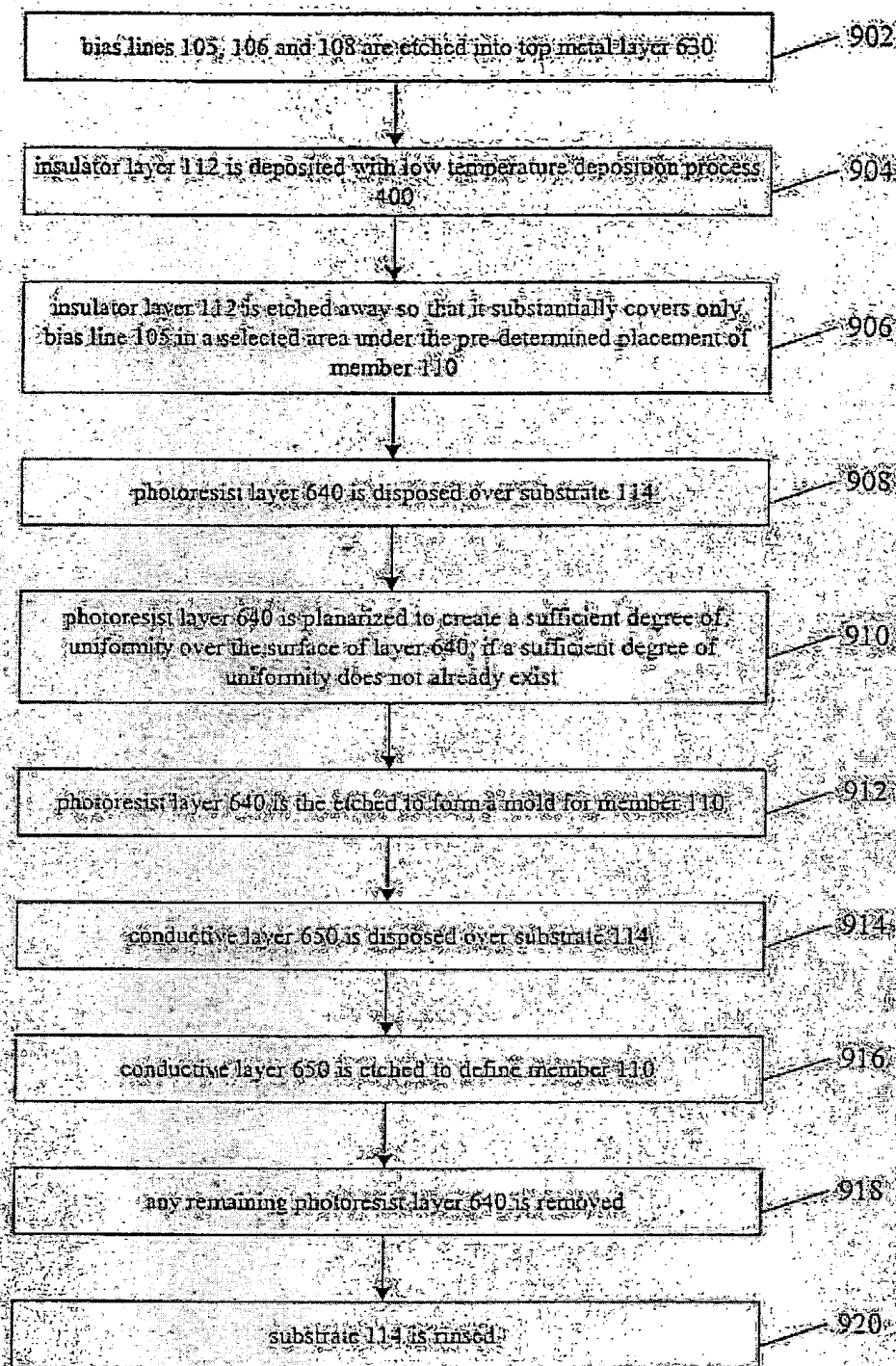
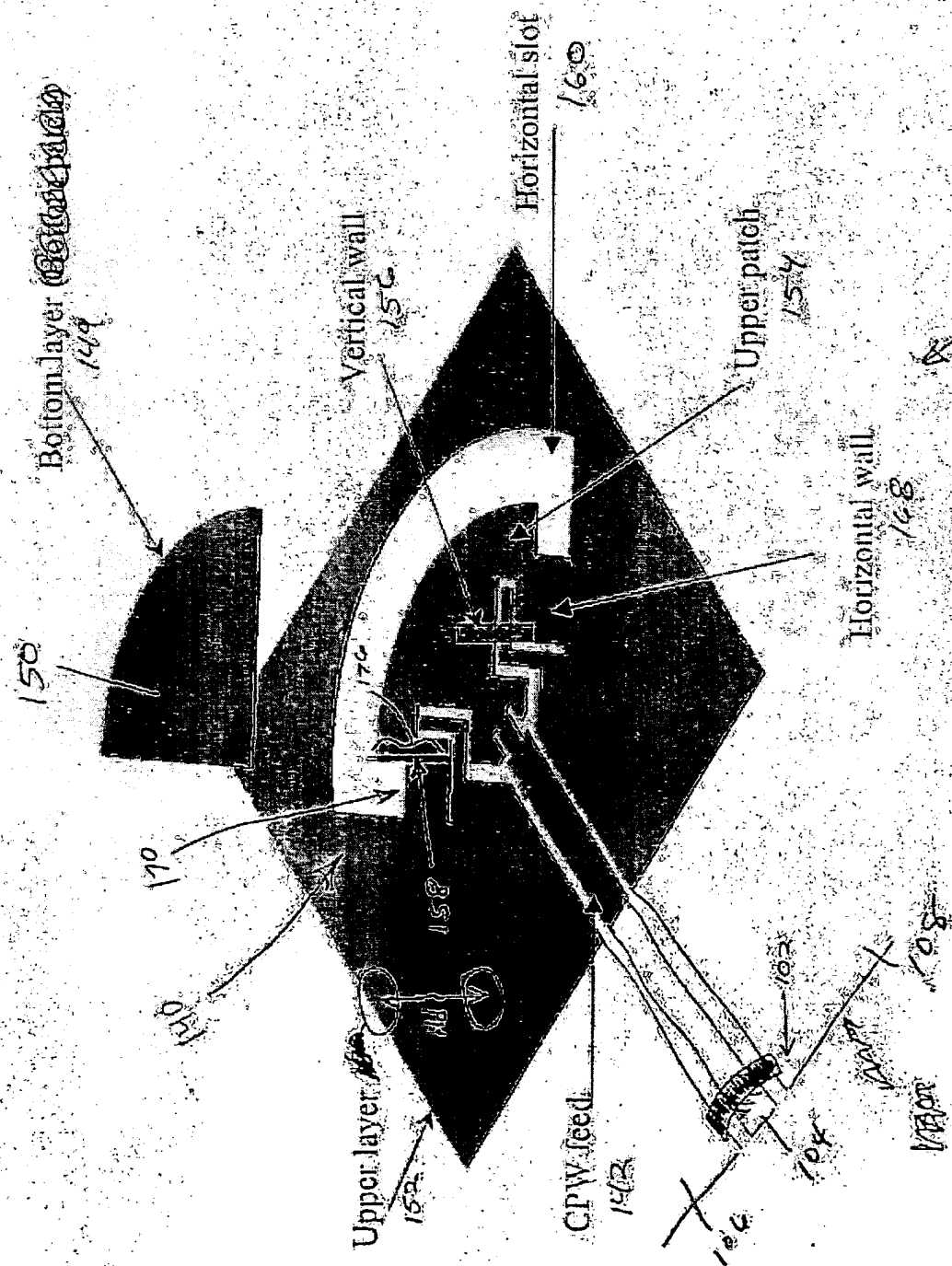


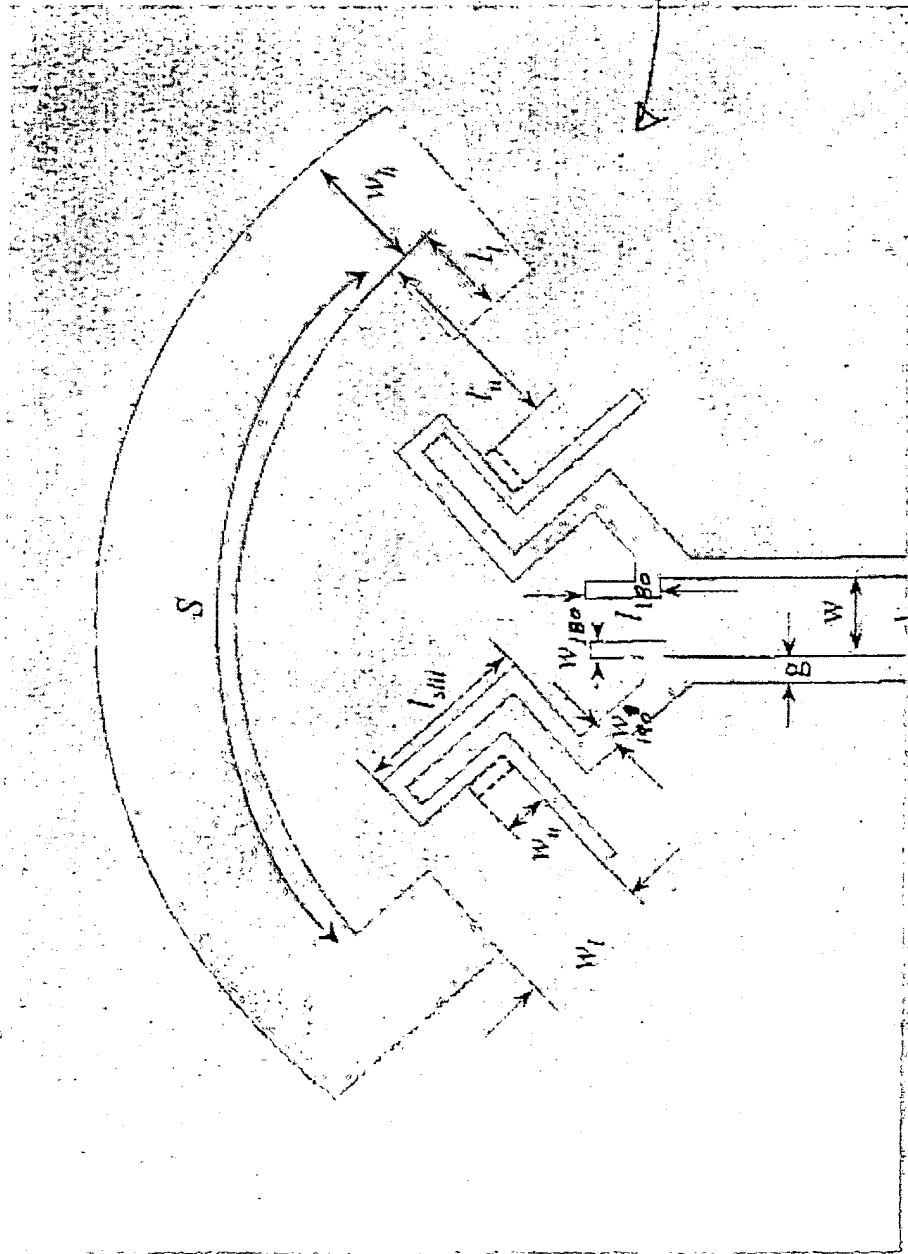
FIG. 9

400



716

130



Feed structure:
CPW width: w
CPW gap space: g
Inductive feed: (w_1, l_1, w_2)

140

FIG. 11A

- 160 Horizontal slot width: w_h
- 160 Horizontal slot length: $S_f = S + 2l_1$
- 170 Vertical slot length: $S_v = S + 2l_2$
- 180 Horizontal wall length: w_1
- 190 Vertical wall length: w_2
- 200 Height of the vertical wall (PCB substrate thickness): h_1

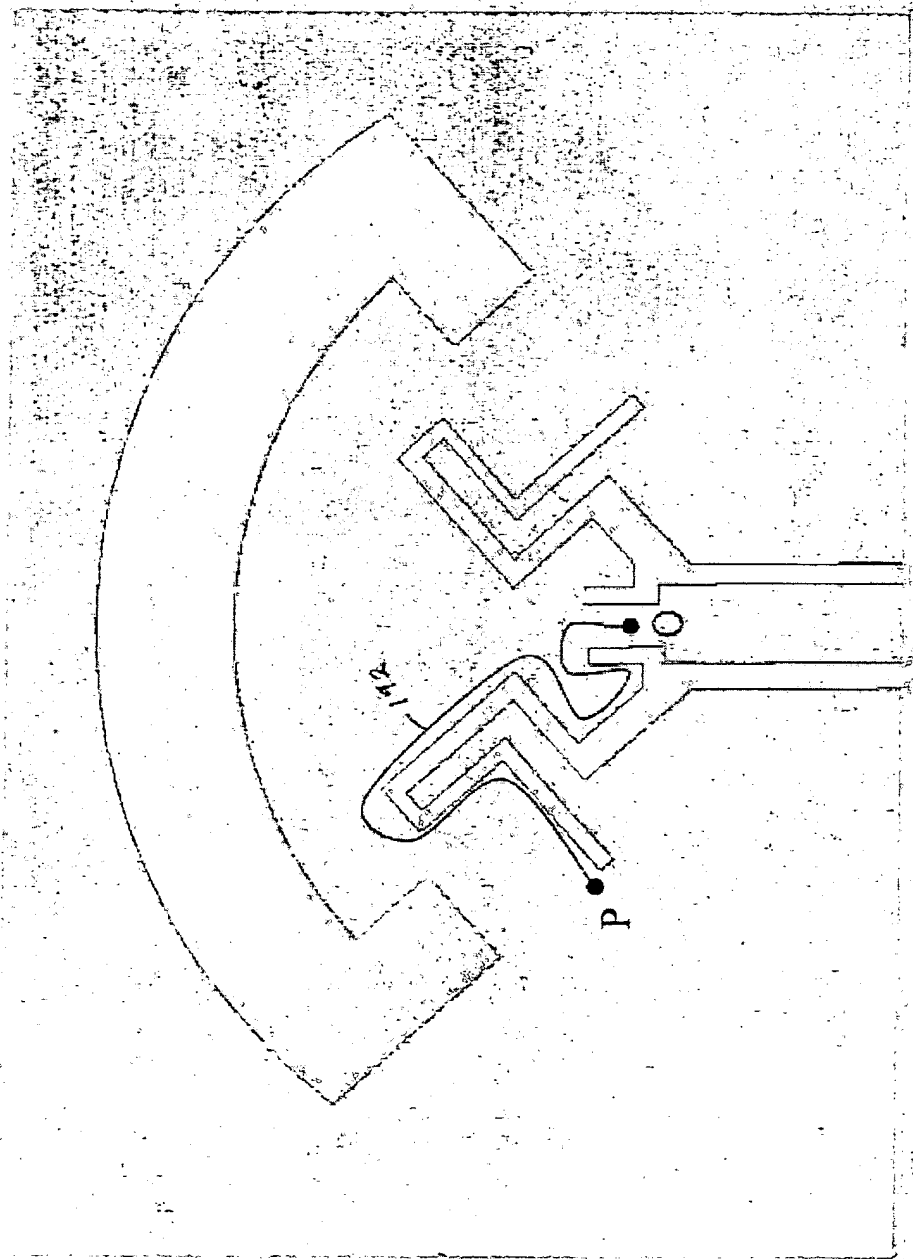
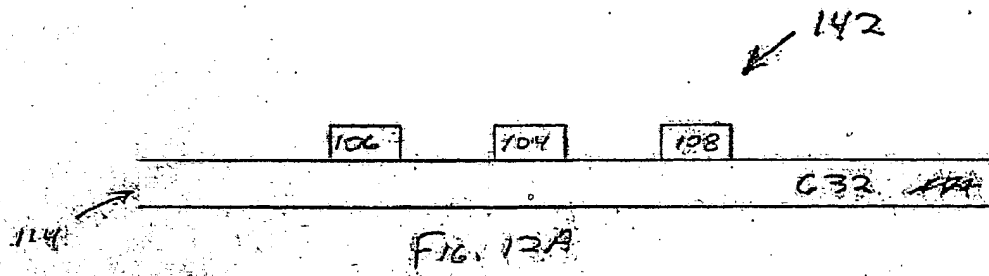
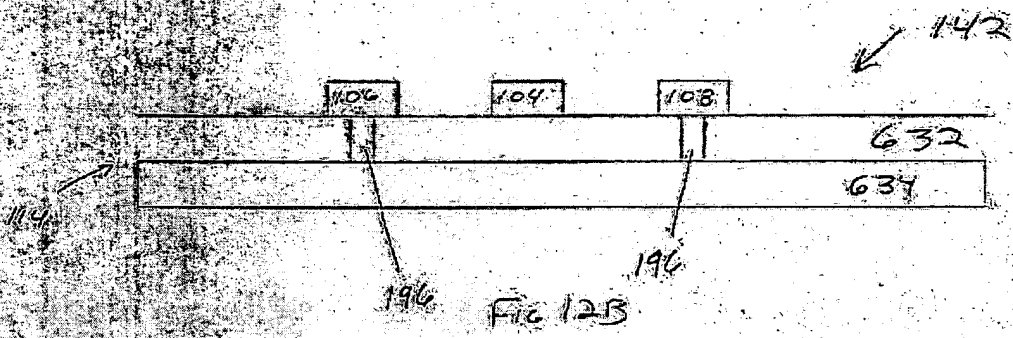


FIG. 11B

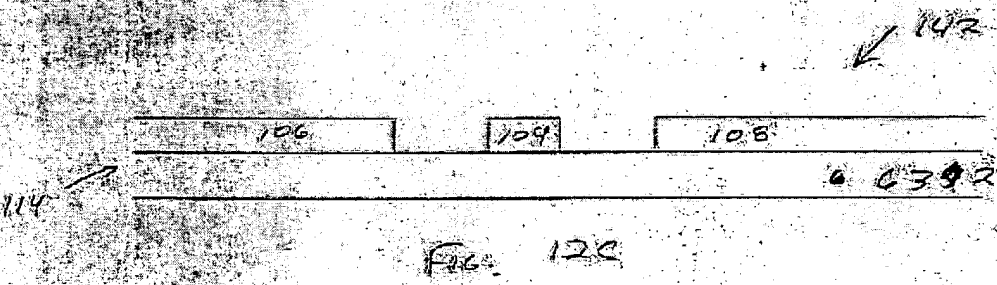
FIG. 11B



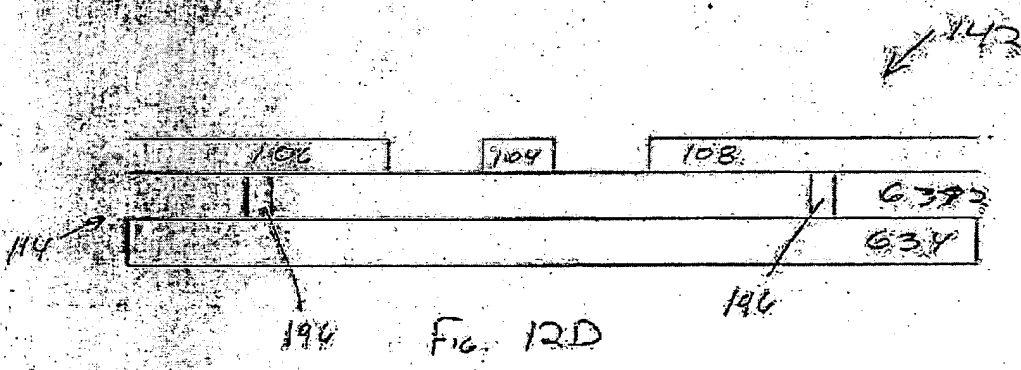
CPS
L = W



G-CPS
L = W



CPW
L >>> W
(Infinit)



G-CPW
L >>> W
(Infinit)

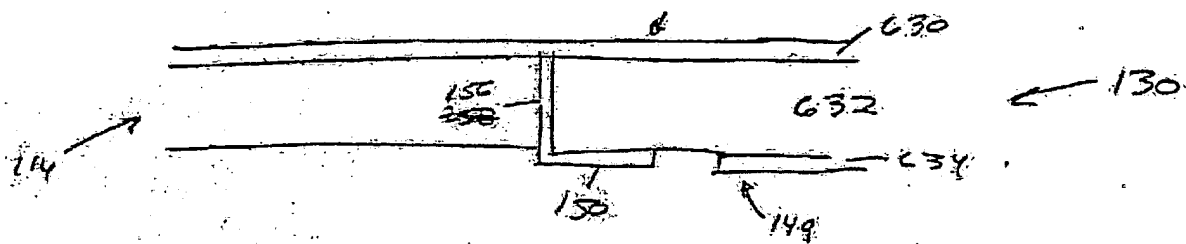


Fig 13A

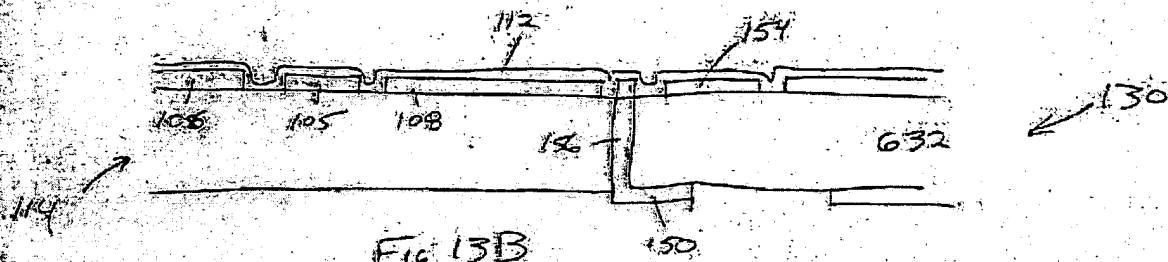


FIG 13B

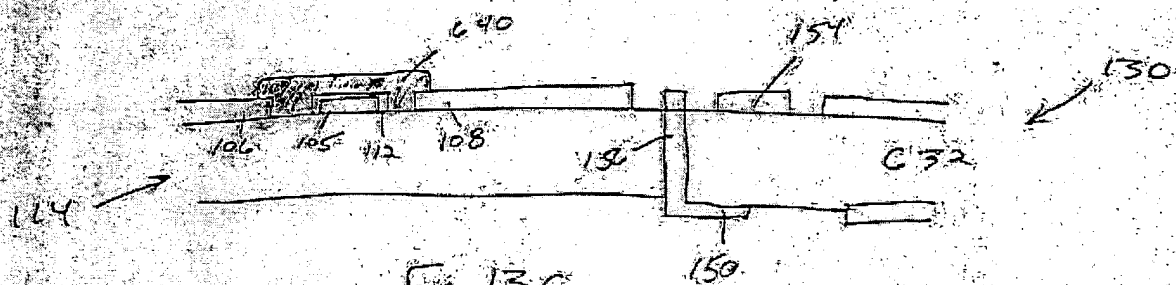
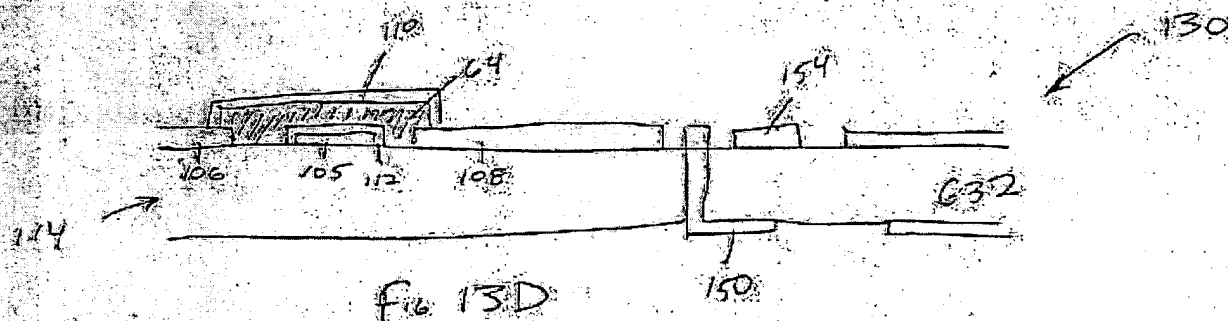


Fig 13C



F16 13D

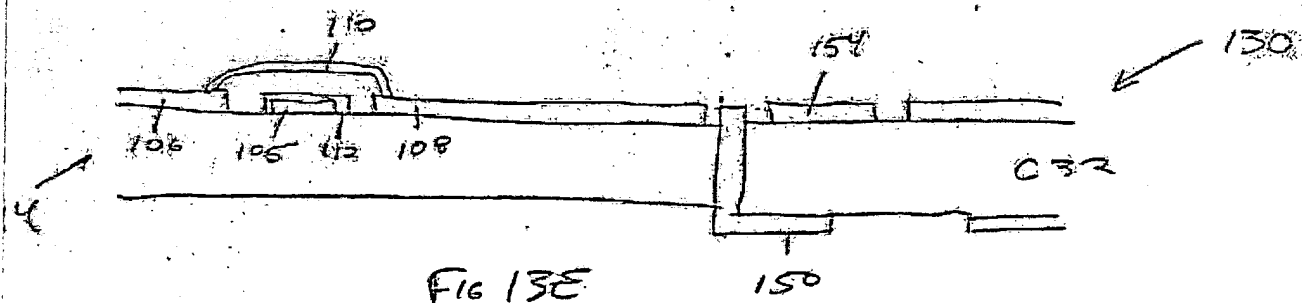


FIG 13E

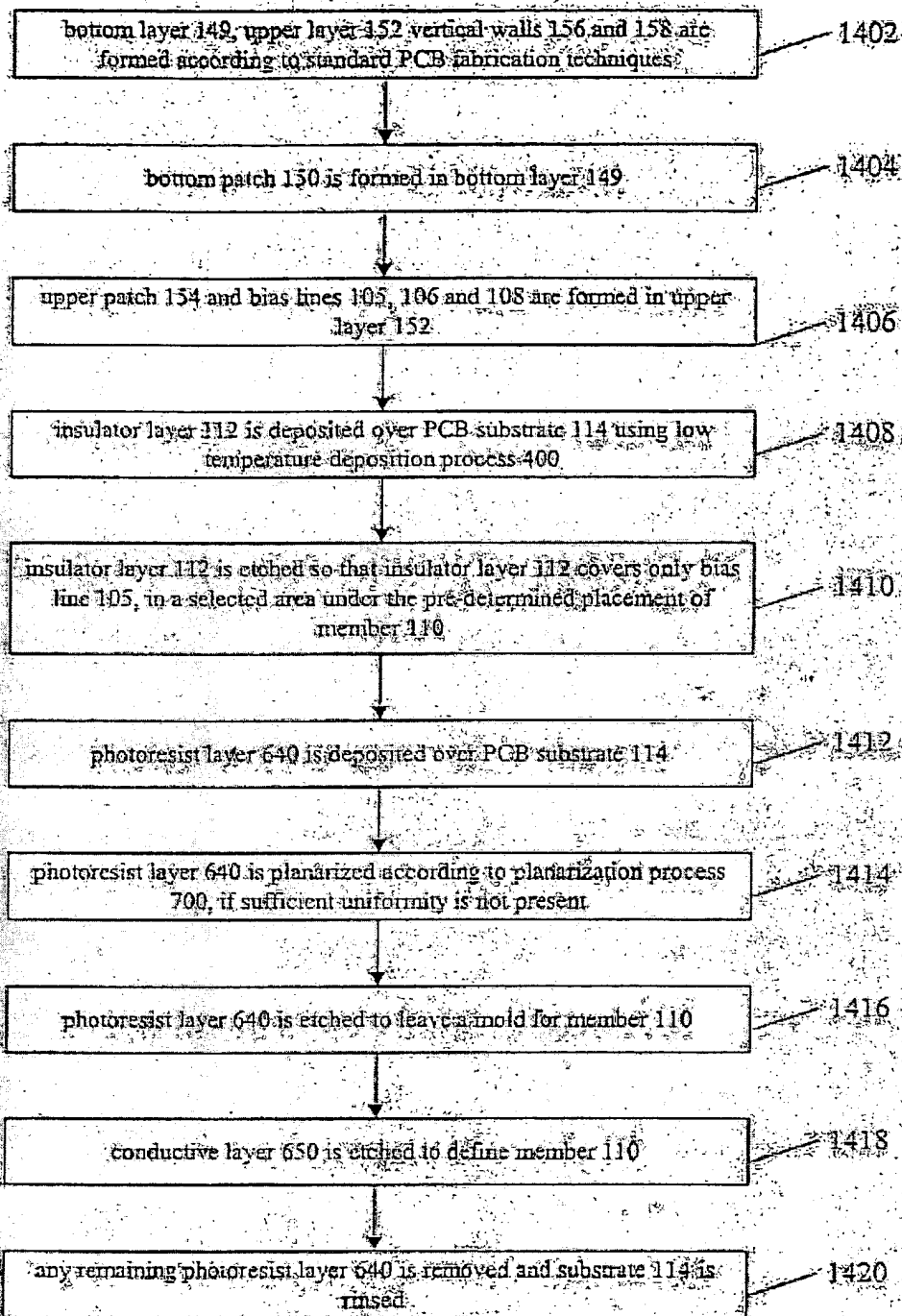


FIG. 14

1400